

Image store locations on P2; subblock 60

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All locations are read/write unless otherwise specified (or reserved)

6000	-	LSTB0
6001	-	LSTB1
6002	-	PSTB0
6003	-	PSTB1
6004	-	Reserved
6005	-	Reserved
6006	-	Handkeys (read only)
6007	-	Machine type (read only)
6008	-	Hooter (read zero, write bit 2**0 to set/clear hoot)
6009	-	Broadcast system errors (bit 2**0) (read and write)
600A	-	System interrupt mask
600B	-	Communications SMAC number
600C	-	RTCX
600D	-	RTCY
600E	-	RTCZ
600F	-	Clock module number
6010	-	Reserved
6011	-	CPU mode and control
6012	-	Reserved
6013	-	Cause system error (read zero, write to bit 2**7)
6014	-	Block 0 activate (write only)
6015	-	Reserved
6016	-	Upper lights (write only)
6017	-	Lower lights (write only)
6018	-	Stop machine (write only)

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## Details of image store X6011 on P2

### Image store read

A read returns bits 2\*\*15 to 2\*\*0 of the OCP control register (working store X27).

### Image store write

The effect of a write is controlled by bits 2\*\*16 and 2\*\*17 of the operand. The combined values of these two bits have the following effect:

- 00 => Bits 2\*\*0 to 2\*\*15 of operand replace corresponding bits in the control register
- 01 => Bits 2\*\*0 to 2\*\*15 of operand are 'or'd into the control register
- 10 => Bits 2\*\*0 to 2\*\*15 of operand are cleared in the control register
- 11 => Illegal; gives program error 9, subclass 4

### Meaning of bits in control register

- 2\*\*0 - Inhibit full photograph
- 2\*\*1 - Inhibit photograph
- 2\*\*2 - Not used
- 2\*\*3 - Inhibit data slaves
- 2\*\*4 - Not used
- 2\*\*5 - Inhibit retries
- 2\*\*6 - Not used
- 2\*\*7 - Inhibit hamming correction reporting
- 2\*\*8 - Not used
- 2\*\*9 - Pull HELP on program error
- 2\*\*10 - Stop on program error
- 2\*\*11 - Not used
- 2\*\*12 - Inhibit photo on soft system error
- 2\*\*13 - Retry in progress
- 2\*\*14 - Stop after photograph
- 2\*\*15 - Error handling in progress

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